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GP 2812

GR 95 P 1411 P

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By: Marshall Nelt

Date: May 26, 2000

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Vannell

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gunther Plasa
Applic. No. : 09/020,872
Filed : February 9, 1998
Title : Method for Producing A Memory Cell
Examiner : Walter L. Lindsay Jr.
Group Art Unit : 2812

TC 2800 MAIL ROOM

A M E N D M E N T under 37 C.F.R. § 1.116

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

S i r :

Responsive to the final Office action dated March 1, 2000,
kindly amend the above-identified application as follows:

In the Claims:

Claim 1 (amended). In a method for producing a memory cell
having a transistor and a capacitor in an integrated circuit,
the improvement which comprises:

initially providing a whole-area polysilicon layer;

C1
Cont

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